AMBIENT TEMPERATURE EFFECT ON SILICON PHOTOVOLTAICS UNDER SIMULATED ENVIRONMENTS


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Graphical abstract

Abstract

Solar energy is a significant renewable source for home and commercial applications. These solar technologies behave differently depending on the ambient temperature surrounding the devices. Thus, the varying ambient temperature necessitates research into the efficacy of various solar technologies under real-life circumstances. In this study, three types of solar technology were studied, which were polycrystalline, monocrystalline, and amorphous silicon photovoltaics (PVs). All the PVs were tested under various simulated environments (hot, room, and cold temperatures). Additionally, real environmental condition tests under direct sunlight successfully depicted the relationship between solar irradiance and ambient temperature on the PVs. Overall, monocrystalline PV outperformed polycrystalline PV, whereas amorphous PV performed poorly. This observation was evident in the lowest performance reduction of monocrystalline PV in hot (power, $P_{pv} = 37\%$), room ($P_{pv} = 82\%$), and cold ($P_{pv} = 95\%$), and direct sunlight ($P_{pv} = 72\%$) conditions. Hence, this research could address the importance of selecting PVs in real-life environments in producing efficient solar PV technologies.

Keywords: Solar energy, monocrystalline silicon, polycrystalline silicon, amorphous silicon, ambient temperature, photovoltaics

Abstrak

Tenaga suria ialah sumber boleh diperbaharui yang penting untuk aplikasi rumah dan komersil. Teknologi suria ini berkelakukan berlainan bergantung pada suhu ambien yang mengelilingi peranti. Oleh itu, suhu persekitaran yang berbeza memerlukan penyesuaian terhadap keberkesanan pelbagai teknologi...
1.0 INTRODUCTION

Over the years, the interest in solar panels for converting light to electrical energy has surged [1, 2]. In the current development of solar panels, the design of photovoltaics (PVs) is categorised into several types, such as monocrystalline silicon [3, 4], polycrystalline silicon [5, 6], amorphous thin-film silicon [7], dye-sensitised [8, 9], thin film [2, 10, 11], and perovskite solar cells [12]. In 1941, Ohl et al. described silicon cells, which were structured by using melt-grown junctions [13]. The related devices demonstrated a power conversion efficiency (PCE) value as low as 1%. Nonetheless, the rapid evolution of PVs depicted massive improvements, in which multi-crystalline cells were utilised to produce PCE values as high as 25% [10]. Hence, the successful discovery of PCE enhancement was owed to the different structures and textures of the fabricated cells.

One of the most reliable and popular types of PVs in current technology was demonstrated by the crystalline silicon PV cells. Concerning the PV structure, crystalline silicon PV cells were produced by selecting one of two boron-doped p-type silicon substrates. Therefore, the doping process resulted in the fabrication of monocrystalline and polycrystalline PVs. Monocrystalline PVs were typically fabricated using pseudo-square silicon wafer substrates cut from column ingots grown through the Czochralski process [14]. Alternatively, the polycrystalline PVs differed in their source, which they were fabricated through grown ingots in quartz crucibles. In terms of the typical sizes for both monocrystalline and polycrystalline PVs, their sizes were approximately 5 and 6 inches, respectively.

Thin-film solar cells were another type of PV widely applied in solar energy harvesting technology. The thin-film devices were configured in one of two structures known as substrate or superstrate [15]. The substrate configuration described the structures as metallic coatings on glass or polymer substrates that behaved as the contact. Meanwhile, the superstrate configuration revealed transparent substrates and a conducting oxide coating on the substrates fabricated the connection. Furthermore, both different structures behaved differently in various weather conditions.

An in-depth study entailing various solar classifications based on fabrication materials revealed that every class from a specific material generated different efficiency percentages [16]. Thus, these findings motivated the objectives of this study in testing several PVs and their corresponding electrical characteristics. Multiple literature studies utilised different methodologies and setups for prototype studies. For example, incandescent, halogen, and low-consuming fluorescent lamps were used to simulate the luminance of sunlight in providing a full spectrum [17]. On the contrary, the light source’s colour temperature must be considered when providing the sunlight simulation. For instance, 4800 to 5000 K provided the most precise colour temperature closer to the direct sun. The 5000 to 6800 K range also represented the change in daylight colours [18].

Based on a PV study, both hot and cold weather conditions affected the voltage ($V_{oc}$) and current ($I_{sc}$) values [19]. Generally, these values depicted decreasing behaviours in both situations. Moreover, the $I_{sc}$ value was higher in summer than in winter. In another study, single-crystal PVs were fabricated and successfully assessed outdoors in Brunei [17]. The variable bipolar operational power supply was tested to measure the panels short-circuit current ($I_{sc}$) and open-circuit voltage ($V_{oc}$). Nonetheless, the study concluded that the monocrystalline PVs’ PCE was higher at colder temperatures. In addition, the PCE values decreased once the operating temperature increased.

According to another study, solar irradiance and temperature were highlighted to affect the performance of the amorphous and polycrystalline panels [18]. Therefore, the degradation of these devices was explained in terms of the behaviour of the PVs. One of the main considerations of this study was to investigate the characteristic of PV...
technologies. In Malaysia, studies were performed to test various PV performances [19]. A characteristic curve was obtained, which led to an increase until a turning point before decreasing. The justification for this behaviour was mostly related to the influence of solar irradiance and temperature. Another study in Algeria evaluated the performance of the monocrystalline solar module under different weather conditions, which demonstrated the impact of solar irradiance on the performance curve [20]. The performance curve illustrated that the performance reduced during the irradiance peak. Furthermore, the results indicated that the performance dropped during higher temperatures, which depended on the irradiance and the temperature. From the literature studies mentioned above, solar irradiation and ambient temperature were determined to be the most critical factors that could affect the PVs’ performance [21, 22]. A study by Adeeb et al. investigated the necessity of environmental temperature optimisation in PVs [23]. The study concluded that ambient temperature influenced annual energy yield in various PVs, rendering it important for large-scale projects. In addition, high temperatures can increase the lattice parameter due to thermal expansion. Thus, this study aimed to determine the extent to which normal daily temperature fluctuations affected the crystal structure of solar panels, focusing on monocrystalline structures. This study aimed to explore the performance effects of severe ambient temperatures on monocrystalline, polycrystalline, and amorphous PVs. The correct PV selection was necessary to deliver an optimised setting for efficient PVs. Moreover, the study assessed the influence of charge carrier trapping under constant illumination and varied temperatures and the performance of PVs in real-world situations with direct sunshine. These tests aimed to depict the advancements and limitations of PVs over an extended time in a simulated setting. Since the light environments were known to influence these PVs, they were effectively investigated in this study. Hence, this study provided useful insights into the performance of PVs under various environmental situations, which can inform future research and development efforts in the field.

2.0 METHODOLOGY

2.1 Design Structure of Greenhouse Chamber

Monocrystalline (9 V, 3 W), polycrystalline, and amorphous PVs were purchased from SolarPowerItems, China. Initially, the PVs were placed in a chamber to facilitate various ambient temperature values in simulated environments. The main box (greenhouse) was utilised in the chamber, which was designed to conduct the simulation of different ambient temperatures. Additionally, the PVs were mounted on the greenhouse chamber. Subsequently, five high-powered light-emitting diodes (LEDs, specifications = 1 W, 120 Lm/W, 6000 K) were purchased and placed inside the greenhouse to serve as the loads for the PVs. The five LEDs were also connected in parallel with a 3 V supply. A thermoelectric Peltier module (12 V, 10 A) was applied to simulate the cold and hot weather. The simulated weather conditions were performed by cooling and heating the Peltier module’s sides. All the weather simulation components were placed inside the greenhouse chamber to simulate the ambient temperature, as illustrated in Figure 1.

![Figure 1 Diagram representing the greenhouse chamber main design and system layout](image)

2.2 Measurement of PV Performances in Simulated Environments

In the chamber design, the heater, cooler, fan, and light source wire connections were connected within the box’s exterior to avoid interruption during the measurement process. Moreover, the multimeter probes were attached to the load and placed within the box’s exterior to obtain accurate readings. The thermometer was also located near the PV to obtain the ambient temperature values. Five LEDs were seated inside on the top side of the greenhouse model to verify if the installed PV was functioning properly. The temperature was obtained at one-hour intervals to ensure that the solar was under the effect of the current temperature.

During the experiment, eight hours were allocated for each PV test. Weather simulation tests were performed before the experiment to avoid overheating and damage. Subsequently, the operating times to reach the desired temperatures (hot and cold) were noted. The results of the $V_{oc}$ and $I_{sc}$ were measured and recorded for each temperature test. Hence, the output power was calculated from the obtained values. PV modules were also installed in arid, dust-free locations, with regular cleansing and maintenance to improve PV performance.
2.3 Peltier Module of the Greenhouse Chamber

A typical thermoelectric module containing an array of doped bismuth telluride semiconductor pellets was connected electrically in series but thermally in parallel. Therefore, the chamber would have one cold surface and an opposite hot surface in Figure 2. Initially, the heating system was designed where a large bulk heatsink was placed on the hot surface of the Peltier. Subsequently, a small fan was mounted on top of the bulk heatsink to dissipate the heat once the heatsink became hot. Moreover, for the cold ambient temperature simulation, a central processing unit (CPU) heatsink cooler was mounted on the hot surface of the Peltier, where the heatsink did not heat up. Simultaneously, a small heatsink was mounted at the cold surface to absorb the coldness produced by the Peltier. A small fan was mounted on the small heatsink to dissipate the cold air. Finally, the prototype was placed in an enclosed room with a running air-conditioner at the lowest temperature as support.

![Figure 2](image)

**Figure 2** Photographs of the Peltier setup for heating and cooling systems. (a) Peltier setup for the heating system with a heat dissipation fan. (b) Peltier setup for cooling system on a CPU heatsink and a small concentrating heatsink to extract the freezing characteristics.

3.0 RESULTS AND DISCUSSION

Three main conditions were simulated using the built prototype, which produced the weather simulation of hot, cold, and room temperatures. The testing condition for each type of simulated weather was demonstrated under direct sunlight to verify the impact of the solar irradiance. Therefore, the obtained results were focused on the influence of the operating hours, temperature, and solar irradiance on different PVs. Initially, the simulation tests were investigated in a simulated sequence: hot → room → cold. After each ordeal, each PV rested for around 16 hours before conducting the next test.

The temperatures achieved within the simulation box were mainly fixed among all three solar types. The temperatures for the hot and cold conditions were approximately 40 to 42 °C and 18 to 21 °C, respectively. For the room temperature conditions, the temperature was estimated to be approximately 26 to 28 °C. All the tests were performed for 8 hours, from 8.00 pm to 4.00 am (GMT +8). This period was selected to avoid any sunlight interference, and the PVs functioned based on artificial lighting. The experiment was repeated under direct sunlight testing. The chosen period was from 8.00 am to 4.00 pm (GMT +8) to obtain proper solar irradiance variations throughout the day.

3.1 PV Performance under Different Operating Hours

Initially, the influence of various operating hours on PV performance was measured across all devices (monocrystalline, polycrystalline, and amorphous). Generally, the PV performance was represented by the measurement curves in terms of $V_{pv}$, $I_{pv}$, and power ($P_{pv}$). Figure 3 depicts the variation of $V_{pv}$, $I_{pv}$, and $P_{pv}$ with the corresponding different temperatures (hot, room, and cold). Overall, the PV performance degraded for all PVs. The degradation process was probably due to the stress applied to the PVs throughout the operating hours. Moreover, the PV performance degradation process observed from the graphs was a reversible loss, which was reversed by rest.

![Figure 3](image)

**Figure 3** The variation of $V_{pv}$, $I_{pv}$, and $P_{pv}$ throughout the operating hours in (a) hot weather simulation, (b) room temperature simulation, and (c) cold weather simulation. In the $I_{pv}$ and $P_{pv}$ graphs, the left side of the y-scale indicates monocrystalline and polycrystalline PV values, whereas the right side of the y-scale indicates amorphous PV values.
Generally, the $I_{pv}$ and $P_{pv}$ graphs for all PVs in hot (40–42 ℃) and room temperatures (26–28 ℃) demonstrated exponential decay processes. Nevertheless, monocrystalline PV described a slower decay in cold temperatures (18–21 ℃) than in other PVs. Thus, monocrystalline PV was more suitable in cold weather than polycrystalline and amorphous PVs. This decaying process was proposed due to the PVs increasing the $P_{pv}$ loss throughout the operating hours.

According to the charge carriers trapping phenomenon, the PV performance decrease occurred in the surface layers of the PVs [24]. Therefore, the charge carriers trapping process reduced the electron-hole (e-h$^+$) recombination pairs used to generate enough electrical signals. At higher temperatures, the band gap of semiconductor materials narrowed, generating more charge carriers. This process also increased the probability of charge carrier recombination, and multiple studies observed this effect [25,26].

Regarding the phenomenon of charge carrier entrapment, the (e-h$^+$) recombination pairs required to generate sufficient electrical signals decreased, resulting in decreased PV performance. This phenomenon predominantly occurred in the surface layers of PV cells. During operation, more charge carriers became trapped in different energy gap states, reducing PV performance. Conversely, the trapped minority charge carriers retained their original condition after stopping the PV measurement. Hence, the PVs were weakened temporarily, and the process was reversed. It was also worth noting that once the next test was initiated, the PV ratings returned to their original values.

The linear degradation processes were portrayed based on the $V_{pv}$ values for all PVs. Nonetheless, these losses were mainly bearable by all the PVs. At higher temperatures, the impact of these losses was more severe, and with time, the PV performance dropped further. Thus, several effects were causing the reduction in PV performance. Additionally, the operating hours indicated how much the PVs could sustain and withstand the reversible conditions. The reversible losses were considered temperature variations throughout the operating hours, which caused energy losses to the PV panels.

After the study tests ended, the charge carriers returned to their original state and were active in their respective layers. Furthermore, more $P$ loss to the PV performance was introduced with longer operating hours. The long operating hours were considered a factor for the degradation since it allowed different effects on the PVs during the study tests [27]. Eventually, the charge carriers’ trapping process still occurred if the operating hours were stretched along with temperatures, which the PVs recovered from afterwards.

The monocrystalline PV was illustrated to produce the least reduction in PV performance throughout the operating hours. Hence, the monocrystalline PV acquired higher sustainability toward the degradation process than polycrystalline and amorphous PVs. The higher sustainability was due to the crystal structure of the monocrystalline PV and fabrication method, which provided a higher efficiency [28]. Furthermore, the trapping of carriers occurred in every type of PV tested [24]. On the contrary, the charge carrier trappings in the monocrystalline PV produced a weaker influence than the polycrystalline and amorphous PVs. The more invalid result was also probably due to the high crystal structure quality and the uniformity of the layers in monocrystalline PV. Consequently, due to its effective structural engineering, monocrystalline PV remained a better option for operating hours than polycrystalline and amorphous PVs.

### 3.2 PV Performance under Different Temperatures

In this study, the influence of temperature is visualised in Figure 4 for monocrystalline, polycrystalline, and amorphous PVs. Generally, all curves decreased, with the simulated cold environment (26–28 ℃) displaying the best PV performance. On the contrary, PV performance reduction was highest during the simulated hot environment (40–42 ℃). The reversible degradation was depicted as the main influence of this behaviour. The surrounding daily temperature and weather conditions affected the reversible degradation of the system. Hence, the PVs possessed a lower performance in the hot environment. Once the state was removed again, the PVs regained their performance.

Based on the operating temperature variation in Figure 4, the movement of the (e-h$^+$) pairs leads to a variation in the PV performance [29]. The finding indicated that the direction of the electron-hole pairs was highly affected at a higher temperature, which led to a significant drop in the PV performance. In addition, the operating temperature also contributed to the performance reduction due to the charge carriers trapped. Thus, the operating temperature even demonstrated a harsher impact on the charge carriers. The effect was caused by the temperature exciting the charge carriers to move around the PV structures.

At higher temperatures, the movement of charge carriers increased, possibly changing the energy states. Through this process, the charge carriers easily get trapped [30]. Furthermore, the lattice parameter changed at elevated temperatures, and the charged carriers were trapped at higher temperatures [31]. All the charge-trapping effects mostly occurred at the top surface layers, which recovered once the PV rested after the study test. Hence, the resting process aided the recovery process when the minority carriers provided less stress inflicted upon them. The PV returned to its original state, and most carriers were active in the next recombination process. Also, the PV performance loss was demonstrated by the PVs originating from the combination of continuous testing time and
varying operating temperatures. The loss occurred in all PVs during solar energy harvesting and the charge carrier's recombination processes. Moreover, the loss process was accelerated when the increasing temperature contributes more to the charge carriers trapped by providing more energy.

amorphous structure of amorphous PV, the PV acquired more significant charge carrier trapping. When the intrinsic layers exceeded the collection width, the output power density was saturated at a maximum value where the PV was limited in performance [32]. The absorbed power from the optical beam was solidly proportional to the thickness of the PV structure, which affected the carrier’s mobility.

3.3 PV Performance under Direct Sunlight

The peaks of solar irradiance and temperatures were usually obtained from 11.00 am to 2.00 pm (GMT+8) [33]. In the investigation study (see Figure 5), the $V_{pv}$ peaked at almost 11.00 am for monocrystalline and polycrystalline PVs. Subsequently, the performance of the monocrystalline PV decreased after 1.00 pm (peak solar irradiance time). A similar output was observed for polycrystalline PV, except it did not withstand the temperature effect even though the peak solar irradiance was achieved. Initially, the amorphous PV received enough solar irradiance, which then deteriorated with time regardless of the solar irradiance. Hence, the temperature demonstrated a higher impact on the amorphous PV.

Comparing the PVs, the monocrystalline PV produced the least performance reduction among other PV types regarding power rating. Additionally, the polycrystalline PV established a lower performance reduction than the amorphous PV in general. Nevertheless, the variation of the starting points of the PVs was not very critical. Although higher temperatures caused an increase of charge carriers trapped, the monocrystalline structure did not reveal layer defects that trapped more of these carriers. Therefore, the trapped charged carriers in the monocrystalline PV were less than in polycrystalline and amorphous PVs. Based on the amorphous structure of amorphous PV, the PV acquired more significant charge carrier trapping. When the intrinsic layers exceeded the collection width, the output power density was saturated at a maximum value where the PV was limited in performance [32]. The absorbed power from the optical beam was solidly proportional to the thickness of the PV structure, which affected the carrier’s mobility.

Figure 4 The influence of surrounding temperatures (40–42, 26–28, and 18–21 °C) on (a) monocrystalline, (b) polycrystalline, and (c) amorphous PVs

Figure 5 The PV performance results in terms of (a) $V_{pv}$, (b) $I_{pv}$, and (c) $P_{pv}$ for different solar PVs under direct sunlight (33–37°C) in Malaysia. In the $I_{pv}$ and $P_{pv}$ graphs, the left side indicates monocrystalline and polycrystalline PV values, whereas the right side of the y-scale indicates the amorphous PV values.
The observations revealed that the PVs were more sensitive to the peak of the ambient temperature rather than the solar irradiance. Moreover, the $I_{PV}$ graph described a similar concept as $V_{PV}$, where the monocrystalline PV seemed to withstand the temperature effect during the irradiance peak. The polycrystalline PV also peaked at 11.00 am but dropped as the temperature increased, greatly influencing the PV performance. Meanwhile, amorphous PV did not follow the solar irradiance trend. Additionally, the amorphous PV could not withstand much when the temperature increased. Lastly, the $P$ graph also illustrated a similar behaviour, where the monocrystalline PV's performance reduction was less than that of the polycrystalline and amorphous PVs. Therefore, solar irradiance produced an impact on the PV performance curve.

The trend of increasing to a peak level before decreasing was due to less irradiance spread at the end of the test day. The solar irradiance had driven the PV performance curve to depict a variety of sunlight distribution across the PVs. Nevertheless, solar irradiance was not the leading influence for the later part of the curves’ characteristics. The temperature effect provided a decent amount of impact on the PV performance curves by exhibiting the PV performance reduction even during the irradiance peak. Similarly, the test under the direct sun introduced charge carrier trapping. When the temperature peaked with the solar irradiance, the PV performance dropped due to charge carriers trapped by the temperature’s effect. The temperature produced the charge carriers to be excited and to move randomly within the structure, which got trapped in a gap state. Thus, the trapped charge carriers no longer contributed to the recombination process.

Referring to the monocrystalline PV curves, the structure of the monocrystalline PV was slightly affected by the trapping of charge carriers during the peak of both solar irradiance and temperature. Moreover, the polycrystalline PV curves also revealed the impact of charge carriers’ trapping. In contrast, the effect was more severe than the monocrystalline PV due to crystal structure and quality. Finally, the worst impact occurred in amorphous PV curves where the charge carrier trapping occurred almost from the beginning of the test. Thus, this observation indicated that the uniformity of the crystal in amorphous was low, and there were more carriers trapped, which led to low PV performance [34].

### 3.4 Performance Comparison between Various PVs

The reduction percentage was calculated to determine which PV type performed better along the whole measurements to compare different solar PVs. All the calculated percentages are tabulated in Table 1. Based on the results, the bar chart is plotted in Figure 6. Considering the PV performance, the monocrystalline PV produced the least performance reduction percentages among the other types. The lowest PV performance reduction percentage for the monocrystalline PV was during the simulated cold test (see Figure 6a), with only a 16% performance reduction in the $I_{PV}$ value. Nevertheless, the highest performance reduction percentage for the monocrystalline PV was observed at the simulated hot test (see Figure 6c), in which the $P$ reduction reached 95%. Additionally, polycrystalline PV acquired more advantages over amorphous PV.

#### Table 1 Summary of the reduction performance percentages ($V_{PV}$, $I_{PV}$, and $P_{PV}$) of different PV panels under different simulated weather and real environmental conditions

<table>
<thead>
<tr>
<th>Simulated weather conditions</th>
<th>Parameters (Reduction performance percentages)</th>
<th>Monocrystalline</th>
<th>Polycrystalline</th>
<th>Amorphous</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cold temperature (18°C to 21°C)</td>
<td>$V_{PV}$ (%)</td>
<td>25</td>
<td>69</td>
<td>71</td>
</tr>
<tr>
<td></td>
<td>$I_{PV}$ (%)</td>
<td>16</td>
<td>65</td>
<td>77</td>
</tr>
<tr>
<td></td>
<td>$P_{PV}$ (%)</td>
<td>37</td>
<td>83</td>
<td>93</td>
</tr>
<tr>
<td>Room temperature (26°C to 28°C)</td>
<td>$V_{PV}$ (%)</td>
<td>49</td>
<td>82</td>
<td>79</td>
</tr>
<tr>
<td></td>
<td>$I_{PV}$ (%)</td>
<td>66</td>
<td>75</td>
<td>76</td>
</tr>
<tr>
<td></td>
<td>$P_{PV}$ (%)</td>
<td>82</td>
<td>96</td>
<td>95</td>
</tr>
<tr>
<td>Hot temperature (40°C to 42°C)</td>
<td>$V_{PV}$ (%)</td>
<td>72</td>
<td>91</td>
<td>95</td>
</tr>
<tr>
<td></td>
<td>$I_{PV}$ (%)</td>
<td>83</td>
<td>98</td>
<td>90</td>
</tr>
<tr>
<td></td>
<td>$P_{PV}$ (%)</td>
<td>95</td>
<td>99</td>
<td>99</td>
</tr>
<tr>
<td>Direct sunlight (33°C to 37°C)</td>
<td>$V_{PV}$ (%)</td>
<td>44</td>
<td>73</td>
<td>79</td>
</tr>
<tr>
<td></td>
<td>$I_{PV}$ (%)</td>
<td>50</td>
<td>51</td>
<td>73</td>
</tr>
<tr>
<td></td>
<td>$P_{PV}$ (%)</td>
<td>72</td>
<td>87</td>
<td>94</td>
</tr>
</tbody>
</table>

![Figure 6](image_url) Graphs indicating PV performance reduction percentage ($V_{PV}$, $I_{PV}$, and $P_{PV}$) of monocrystalline, polycrystalline, and amorphous PVs in simulated (a) cold weather, (b) room temperature, (c) hot weather, and (d) under direct sunlight.

Although amorphous PV displayed a lower performance reduction percentage than the polycrystalline PV in the simulated room temperature test (see Figure 6b), the polycrystalline PV demonstrated the lowest performance reduction percentage (51%) in the simulated direct sunlight testing (see Figure 6d). Furthermore, the polycrystalline PV observed high-performance
reduction percentages overall, while the highest performance reduction percentage was achieved at the simulated hot temperature test with 99% of power reduction. Lastly, the amorphous PV acquired the most performance reduction percentages. All the power reduction percentages for the amorphous PV were above 90% for all tests, which revealed its disadvantage.

Based on the results, the percentages indicated that the monocrystalline PV performed much better than other PVs. The higher PV performance was due to the crystal formation and the fabrication procedures of the monocrystalline PVs. Moreover, the monocrystalline PV structure possessed sleeker aesthetics and higher electron flow generation [35]. Nevertheless, there was a noticeable performance reduction for all these PV types. The performance reductions were due to many reasons, such as solar irradiance, temperature, minor crystal defects, and other reversible losses like the temporary charge carriers trapping process. The effects on the PV's performance were depicted particularly for the amorphous PV during the simulated hot weather testing.

A large reduction in performance was observed for amorphous PV, which highlighted that the panel had much more stress on the structure layers causing more charge carriers to trap. There was a noticeable difference between the performance reduction percentages compared to the hot ambient temperature simulation and the direct sunlight testing. The temperature variation between these two tests was slightly different, demonstrating that solar irradiance provided better performance than LED powering the PVs. Hence, the positive effect of solar irradiance was concluded through the lower performance reduction percentage in the simulated direct sun test than in the simulated hot test. Nonetheless, the ambient temperature produced an impact under the direct sun that did not allow the performance curve to exactly follow the solar irradiance curve [36,37]. Despite that, the solar irradiance curve provided a better performance as the temperature effect still influenced it under the hot sun.

4.0 CONCLUSION

This study successfully investigated the performance of different solar technologies under various ambient temperatures. Generally, all solar PVs (monocrystalline, polycrystalline, amorphous) degraded throughout operating hours. The monocrystalline PV performed better than polycrystalline silicon PV, while the amorphous silicon PV was inferior under this investigation. The performance degradation of all solar PVs was ascribed to the charge carriers’ trapping in different energy gap states. Additional operating hours resulted in additional carriers’ trapping and further performance reduction. Moreover, the solar tech performance degraded the most at around 40 to 42 °C compared to the simulated room (26–28 °C) and cold temperatures (18–21 °C). All three solar PVs were tested under direct sunlight (33–37 °C), and the results demonstrated that the solar irradiance and temperature under direct sunlight influenced the PV performance. With the help of this research, the impact of ambient temperature could serve as an impetus for additional PV investigations in created simulated environments.

Conflicts of Interest

The author(s) declare(s) that there is no conflict of interest regarding the publication of this paper.

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References


